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APPLICANTS

Shinsuke Sakamoto, Yokohama-shi, JAPAN;
 Akira Yamaguchi, Kitaadachi-gun, JAPAN;

** CONTINUING DATA *****
 This application is a DIV of 10/282,144 10/29/2002 PAT 6,732,344 **EO**

** FOREIGN APPLICATIONS *****
 JAPAN 2001-331294 10/29/2001 **EO**

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 4	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <div style="display: flex; justify-content: space-between;"> <div> Examiner's Signature </div> <div> Initials EO </div> </div>				

ADDRESS
 22850
 OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.
 1940 DUKE STREET
 ALEXANDRIA, VA
 22314

TITLE
 Semiconductor integrated circuit device and standard cell placement design method

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